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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE December 8, 2005	GROUP ART UNIT
	APPLICANT(S): Stephen J. Todd	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,305,389	04-19-1994	Palmer	382/1	08-30-1991
	A2	6,092,154	07-18-2000	Curtis et al.	711/137	11-07-1997
	A3	6,449,696	09-10-2002	Okayasu	711/137	12-23-1998
	A4	6,453,321	09-17-2002	Hill et al.	707/103	02-11-1999
	A5	2002/0002658	01-03-2002	Okayasu	711/137	12-23-1998
	A6	2003/0126116	07-03-2003	Chen et al.	707/3	12-28-2001

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
						YES	NO
	A7	WO 01/50275	07-12-2001	Europe	G06F 12/08		

NON-PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)
	A8	Jack L. Lo et al. "An Analysis of Database Workload Performance on Simultaneous Multithreaded Processors" Computer Architecture, 1998. Proceedings, The 25 th Annual International Symposium on Barcelona, Spain 27 June – 1 July 1998, Los Alamitos, CA, USA, IEEE Comput. Soc., US, 27 June 1998, pages 39-50, XP010291385.
	A9	Yan Solihin et al. "Using a User-Level Memory Thread for Correlation Prefetching" Proceedings of the 29 th Annual International Symposium on Computer Architecture,

EXAMINER /Matthew Brophy/ (02/28/2008)	DATE CONSIDERED 02/28/2008
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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	2002, pages 171-182, XP002301172.
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